

(TOP VIEW)

10	VDD	GND			VDD	GND	VDD					VDD	VDD	GND	VDD
9															
8	VDD	CMD	VDD	GND	GNDa	GNDa	VDD	VDD	GND	RQ3	GND	VDD	GND	GND	VDD
7	DQA8	DQA7	DQA5	DQA3	DQA1	CTMN	CTM	RQ7	RQ5	RQ3	RQ1	DQB1	DQB3	DQB5	DQB8
6															
5															
4	GND	DQA6	DQA4	DQA2	DQA0	CFM	CFMN	RQ6	RQ4	RQ2	RQ0	DQB0	DQB2	DQB4	GND
3	GND	SCK	VCMOS	GND	VDD	GND	VDDa	VREF	GND	VDD	GND	GND	VDD	SI00	GND
2															
1	VDD	GND		GND	VDD	GND					GND	GND	GND	GND	VDD
	A	B	C	D	E	F	G	H	J	K	L	M	N	P	S

Fig. 2

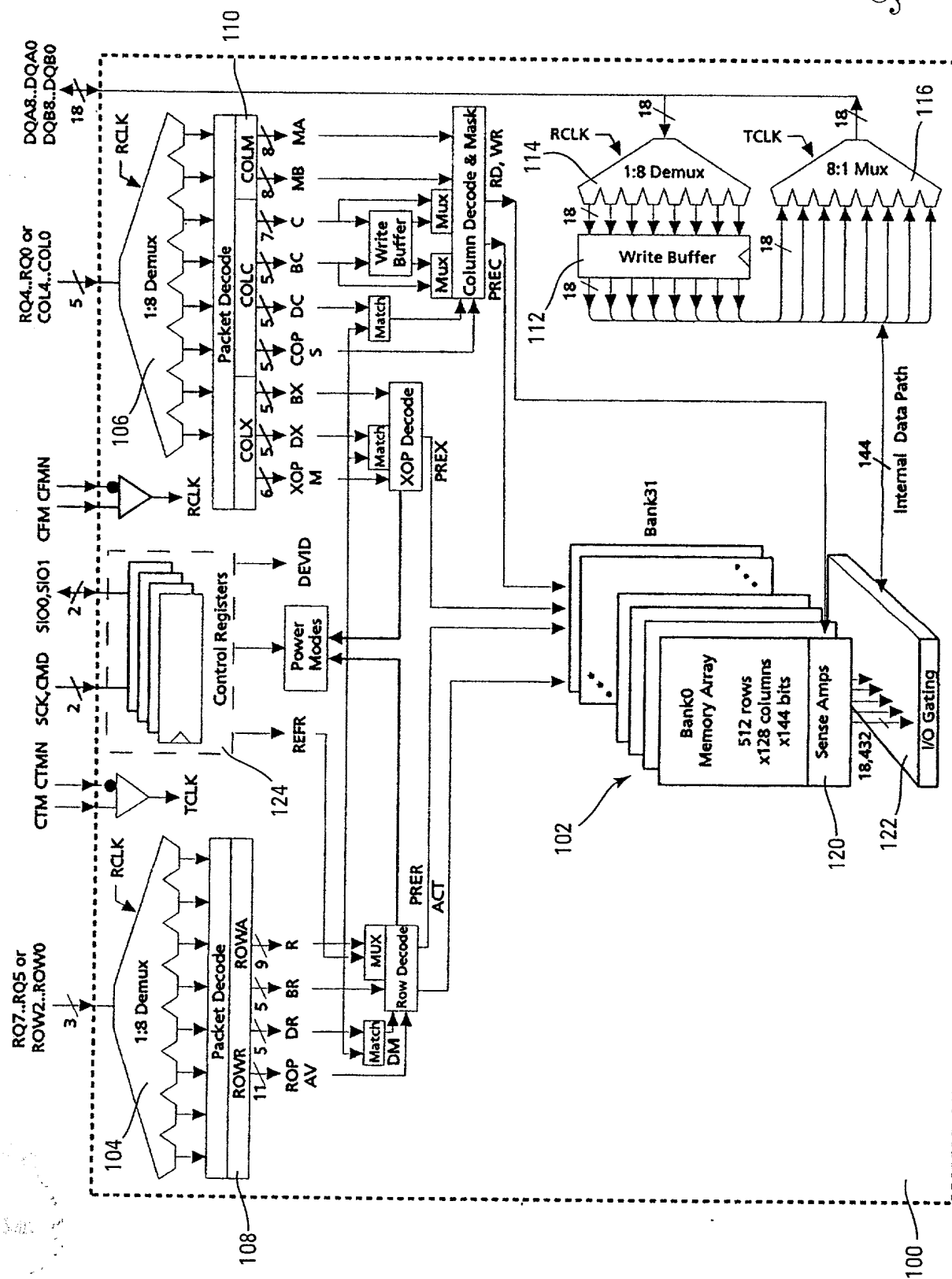


Fig. 1